#9/EX 9/10/3 Justes

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Robert H. Havemann

Serial No.: 10/001,472

Filed: 11/01/01

Docket No.: TI-27506

Examiner. Wojciechowicz, E.

Art Unit: 2815

FOR: INTEGRATED CIRCUIT HAVING A DOPED POROUS DIELECTRIC AND

METHOD OF MANUFACTURING THE SAME

REQUEST FOR EXTENSION OF TIME

Commissioner of Patents PO Box 1450 Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9319 on

pueline J. Garner, Reg. No. 36,144

August 25, 2003

Date

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.

09/16/2003 SSURLES 000000구구인들은 forl대를 Sitension is indicated below:

01 FC:1252 02 FC:1801

One Month (\$110)

X Two Months (\$410)

Three Months (\$930)

Four Months (\$1,450)

Please charge the fee to deposit account no. 20-0868. Any further necessary extension of time is hereby requested. Charge any and all fees to deposit account no. 20-0668. An original and two copies of this sheet are enclosed.

Texas Instruments Incorporated PO Box 655474, M/S 3999 Dallas, TX 75265 (214) 532-9348 Fax: (972) 917-4418

Respectfully Submitted,

ácquéline J. Gamer Reg. No. 36,144